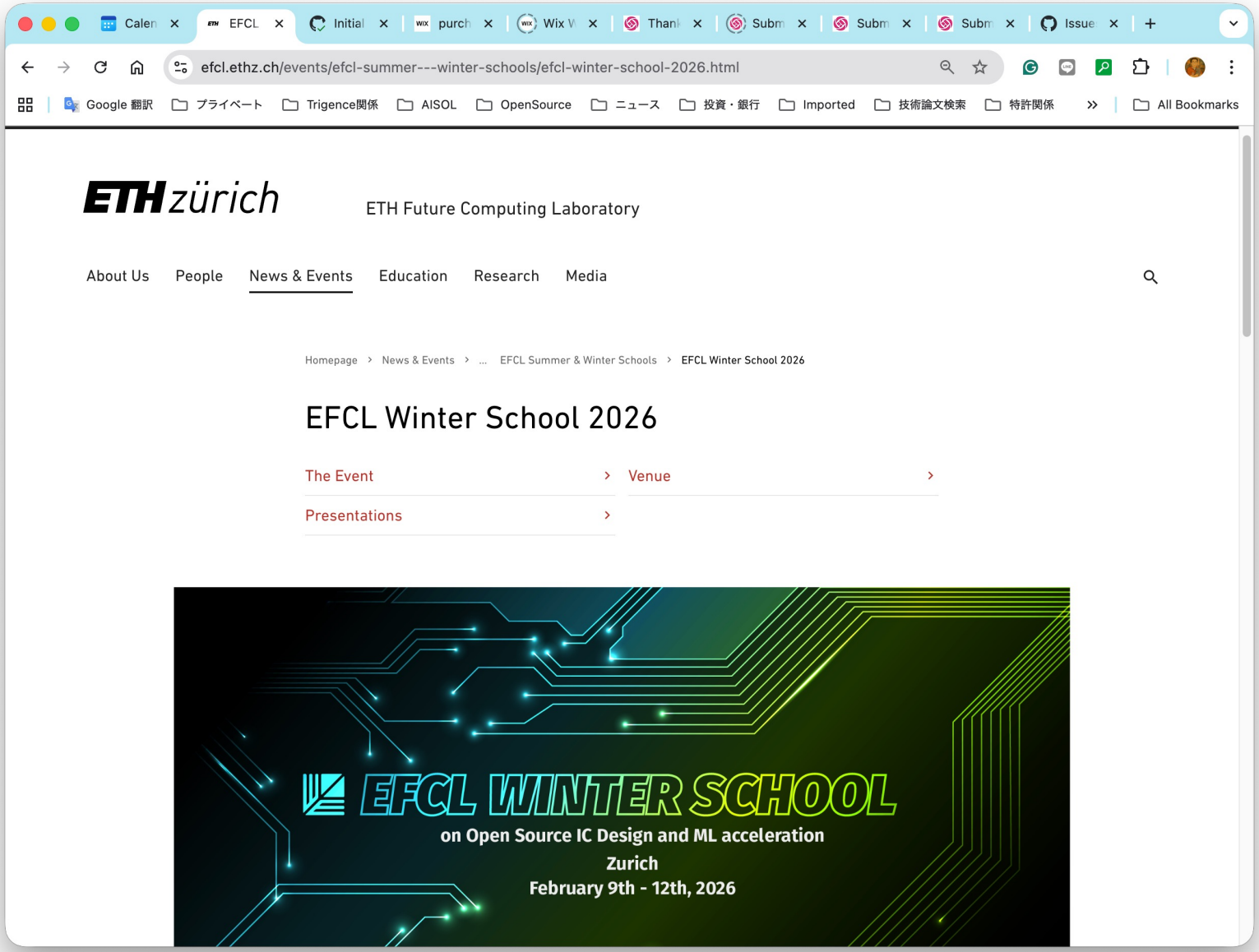
2026年03月25日
ISHI会
説明資料

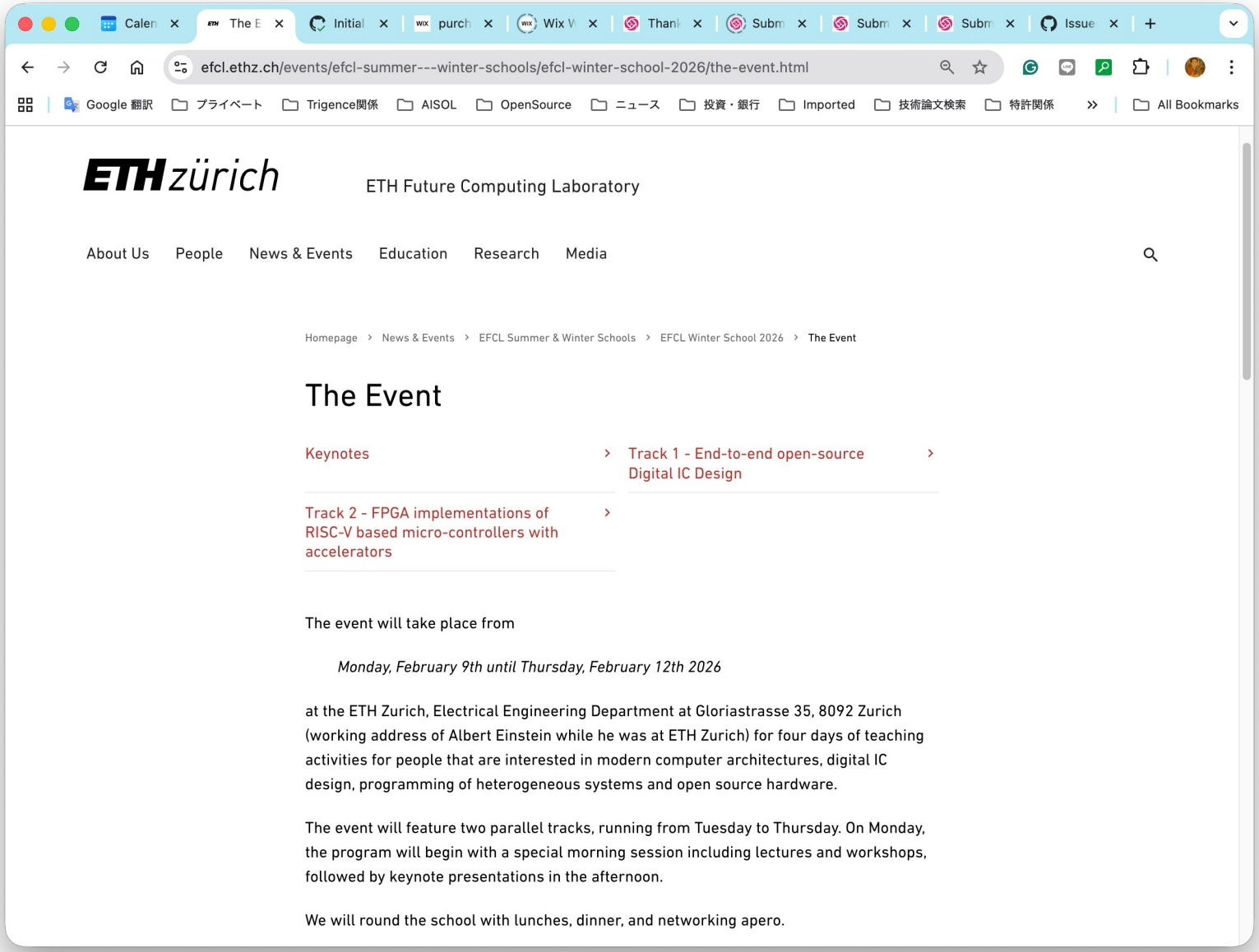
- Open Source Utilized Silicon Initiatives –

AIST Solutions 岡村 淳一

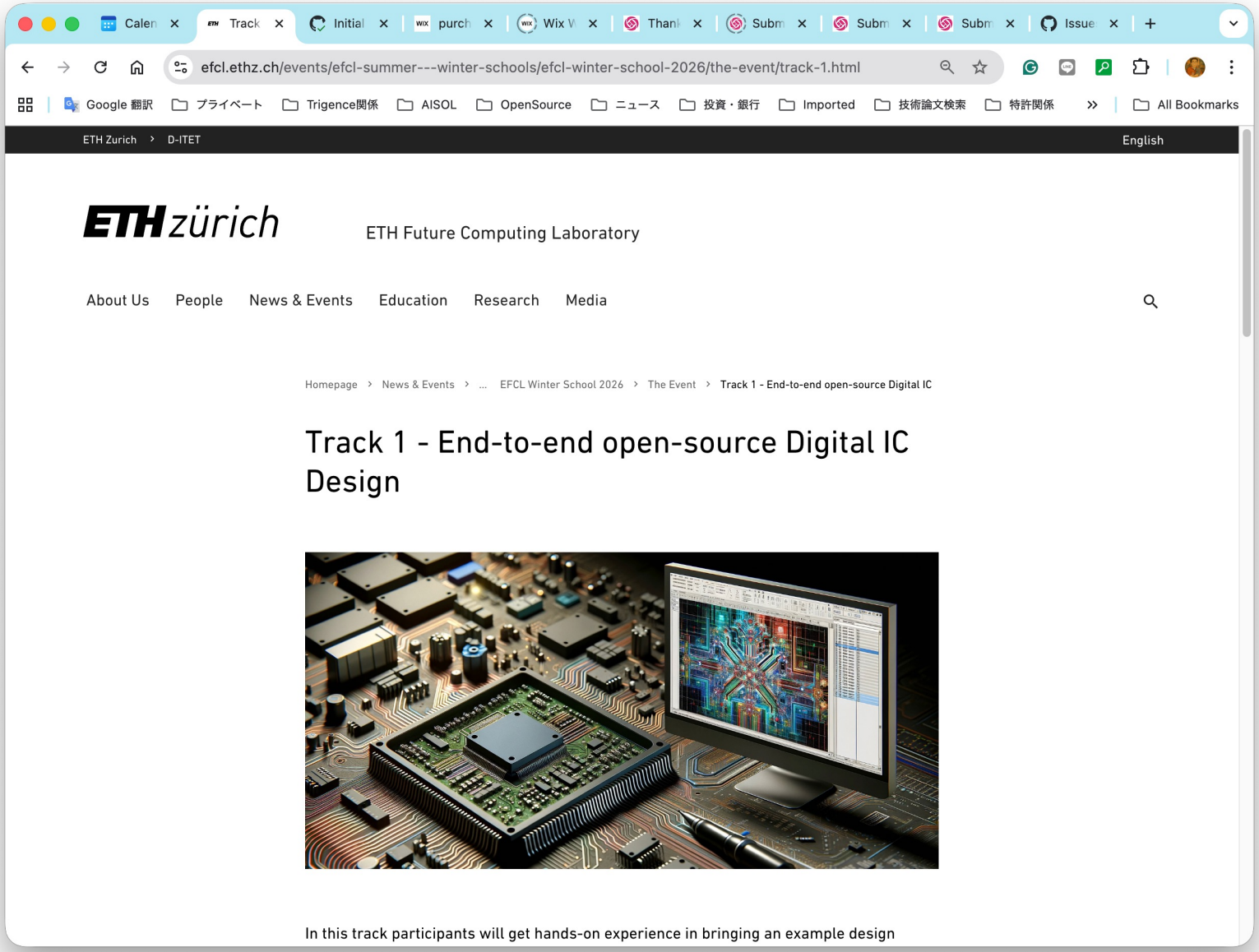
ETH Zurich Winter School



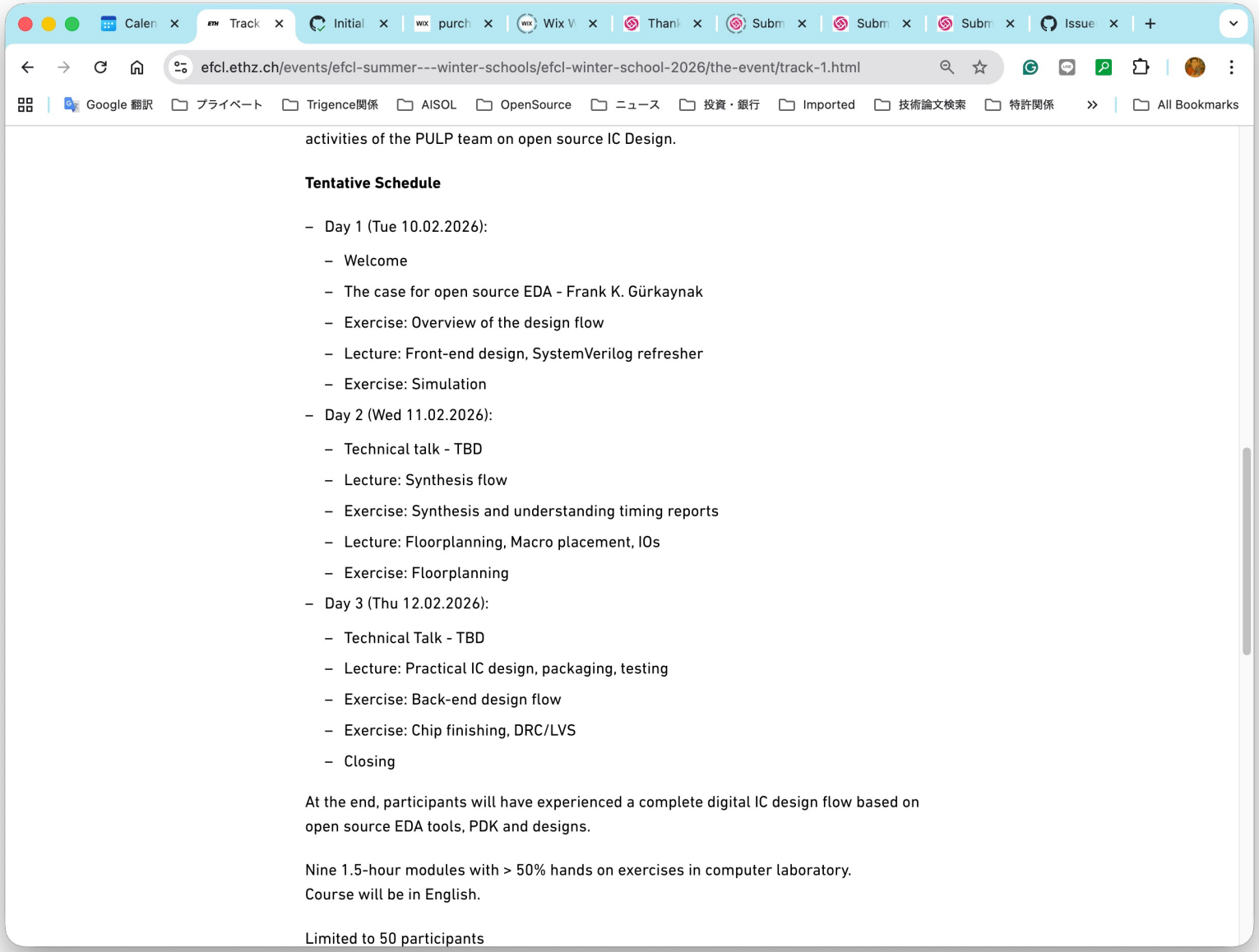
ETH Zurich Winter School



ETH Zurich Winter School



ETH Zurich Winter School



activities of the PULP team on open source IC Design.

Tentative Schedule

- Day 1 (Tue 10.02.2026):
  - Welcome
  - The case for open source EDA - Frank K. Gürkaynak
  - Exercise: Overview of the design flow
  - Lecture: Front-end design, SystemVerilog refresher
  - Exercise: Simulation
- Day 2 (Wed 11.02.2026):
  - Technical talk - TBD
  - Lecture: Synthesis flow
  - Exercise: Synthesis and understanding timing reports
  - Lecture: Floorplanning, Macro placement, IOs
  - Exercise: Floorplanning
- Day 3 (Thu 12.02.2026):
  - Technical Talk - TBD
  - Lecture: Practical IC design, packaging, testing
  - Exercise: Back-end design flow
  - Exercise: Chip finishing, DRC/LVS
  - Closing

At the end, participants will have experienced a complete digital IC design flow based on open source EDA tools, PDK and designs.

Nine 1.5-hour modules with > 50% hands on exercises in computer laboratory.
Course will be in English.

Limited to 50 participants

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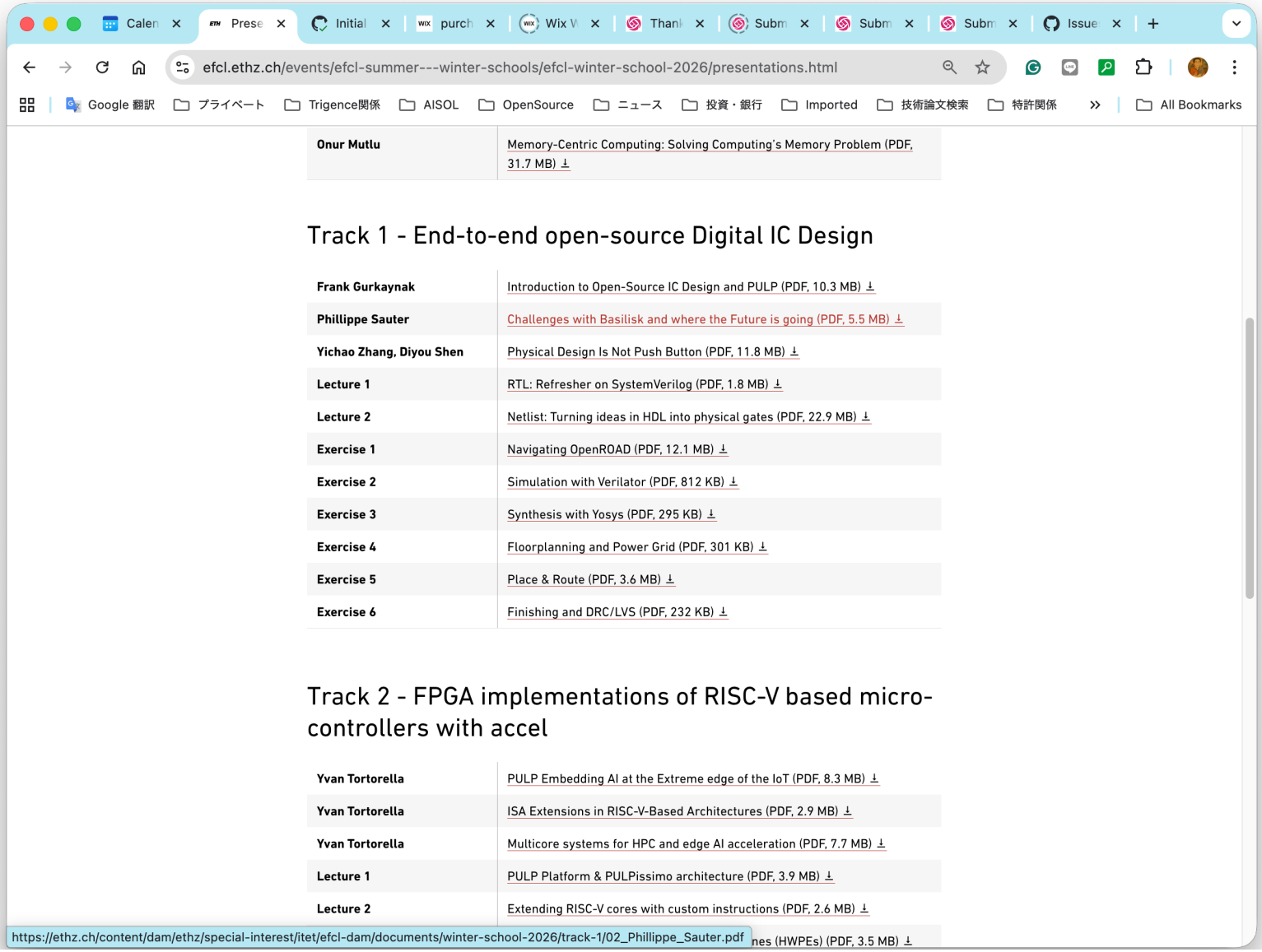

ETH Zurich のオープンソースシリコン・設計スクール

ETH Zurich 半導体設計 Winter School に参加してみた！（１日目）

https://note.com/jun1okamura/n/nf4313755e040

ETH Zurich 半導体設計 Winter School に参加してみた！（２日目）

https://note.com/jun1okamura/n/n8cf557977df9

ETH Zurich 半導体設計 Winter School に参加してみた！（３日目）

https://note.com/jun1okamura/n/nb7a5f89ac6f7

ETH Zurich 半導体設計 Winter School に参加してみた！（４日目）

https://note.com/jun1okamura/n/na5bb98894143